

**INFORMATION DISCLOSURE CITATION**

(Use several sheets if necessary)

Docket Number (Optional)

NEC NEG-239

Application Number

10/022,551

Applicant(s)

SAEKI

Filing Date

December 17, 2001

Group Art Unit

U.S. PATENT DOCUMENTS

| *EXAMINER INITIAL | REF | DOCUMENT NUMBER | DATE | NAME | CLASS | SUBCLASS | FILING DATE IF APPROPRIATE |
|----------------------|-----|-----------------|---------|--------|-------|----------|-------------------------------|
| AS | | 2001/0009384 | 7/26/01 | Saeki | 317 | 113 | |
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FOREIGN PATENT DOCUMENTS

| | REF | DOCUMENT NUMBER | DATE | COUNTRY | CLASS | SUBCLASS | Translation | |
|----|-----|-----------------|----------|-----------------------|-------|----------|-------------|----|
| | | | | | | | YES | NO |
| AS | | 199 22 807 | 11/25/99 | Germany | H04L | 7/033 | | ✓ |
| ↓ | | 100 09 039 | 10/19/00 | Germany | H03K | 5/00 | | ✓ |
| AS | | 2000-252802 | 9/14/00 | Japan (abstract only) | H03K | 5/19 | | ✓ |
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OTHER DOCUMENTS (Including Author, Title, Date, Pertinent Pages, Etc.)

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EXAMINER

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DATE CONSIDERED

2.9.05

EXAMINER: Initial if citation considered, whether or not citation is in conformance with MPEP Section 609; Draw line through citation if not in conformance and not considered. Include copy of this form with next communication to applicant.

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| December 17, 2001 | | | |

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| ✓ | ✓ | Fiedler, A., "A 1.0625GHz Tranceiver with 2x-Oversampling and Transmit Signal Preemphasis", ISSCC 1997, pp 238-239 |
| ✓ | ✓ | Sidiropoulos, S. et al, "A semi-digital delay locked loop with unlimited phase shift capability and 0.008-400 MHz operating range.", ISSCC 1997, pps 332-333 |

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SHEET 1 OF 2

INFORMATION DISCLOSURE CITATION

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*EXAMINER
 INITIAL

OTHER DOCUMENTS (Including Author, Title, Date, Pertinent Pages, Etc.)

"A2BParallel 1.25 Gb/s Interconnect I/O interface with Self Configurable Link and Plesiochronous Clocking", ISSCC 1999, pps 180-181

EXAMINER

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DATE CONSIDERED

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